Simplifying the LC3 instruction set (intro to Reduced Instruction Set Computer, RISC). Note that the LC3 is a RISC machine, but the same principles apply in (1) figuring out how to do in effect more complex instruction execution with simpler instructions and (2) evaluation the performance impacts.

Q. Can we implement JSR and/or JSRR using other instructions? The result would have to have the same, or better, capabilities.

Q. Can we eliminate one or more of the LD variants (LD, LDI, LDR)?

Q. Explain what performance issues your answers to the above two questions bring up. Take a simple, first-look, perspective: do not get confused by the depth and breadth of the general topic.