

Algorithms and Computers

We would like to have:

-- A simple concept of computation/computing/computers

Why?

- 1. When we build one, we can tell what we want: can it do what it is supposed to do?
- 2. When we see one, we can recognize it (eg. is a QM machine a computer?)
- 3. When we look at a complex system, we can identify its fundamental structure: abstraction.
- 4. We can define what we mean by an algorithm (ie., TM that always halts).

Computer Science: The science of computing machines and what they can do?

--- Interesting provided there are interesting machines

--- What's interesting: big, fast, cheap (compared w/ value of what we want to do)

Big: lots of data

Fast: quick results (quick enough)

Cheap: to build, to run, to use.

-- Are there other "computing machines" out there?

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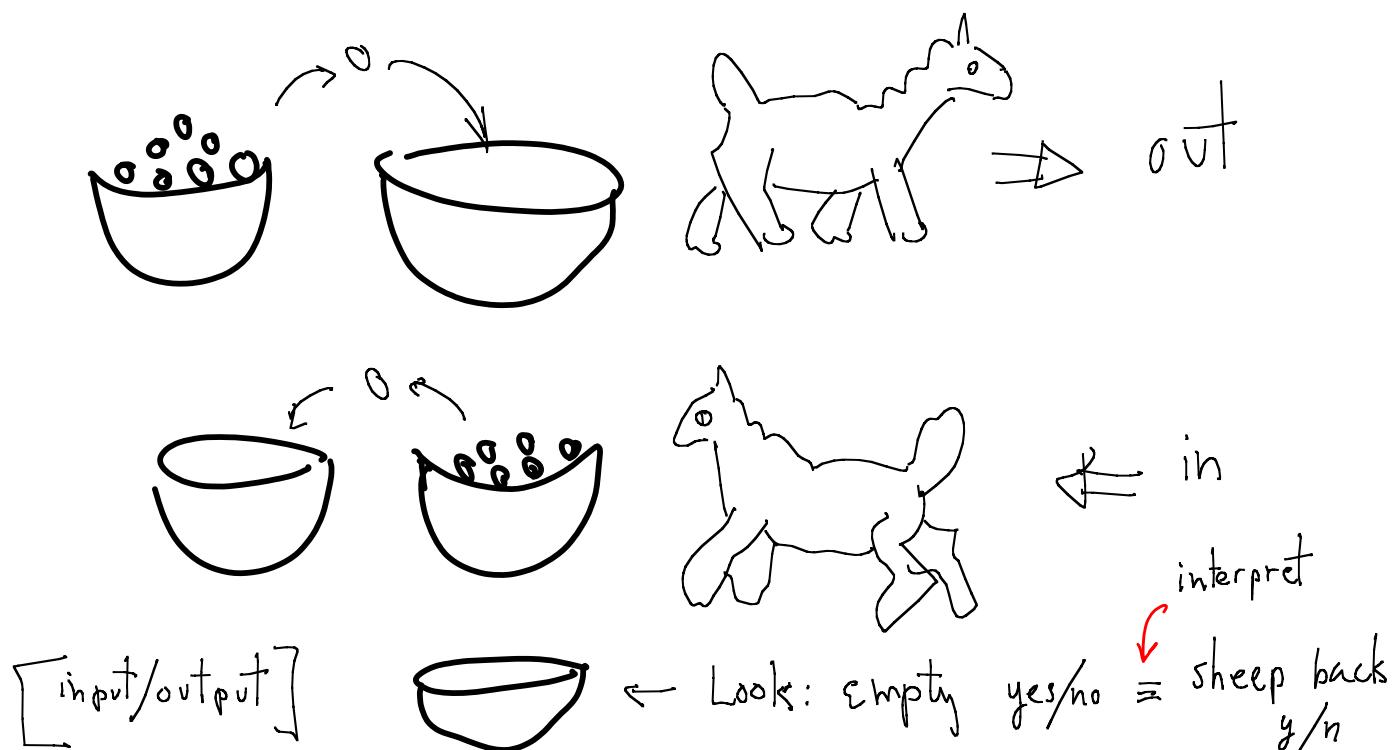
Concept of computation

---- Information (what is it?)

---- Information transformation, results.

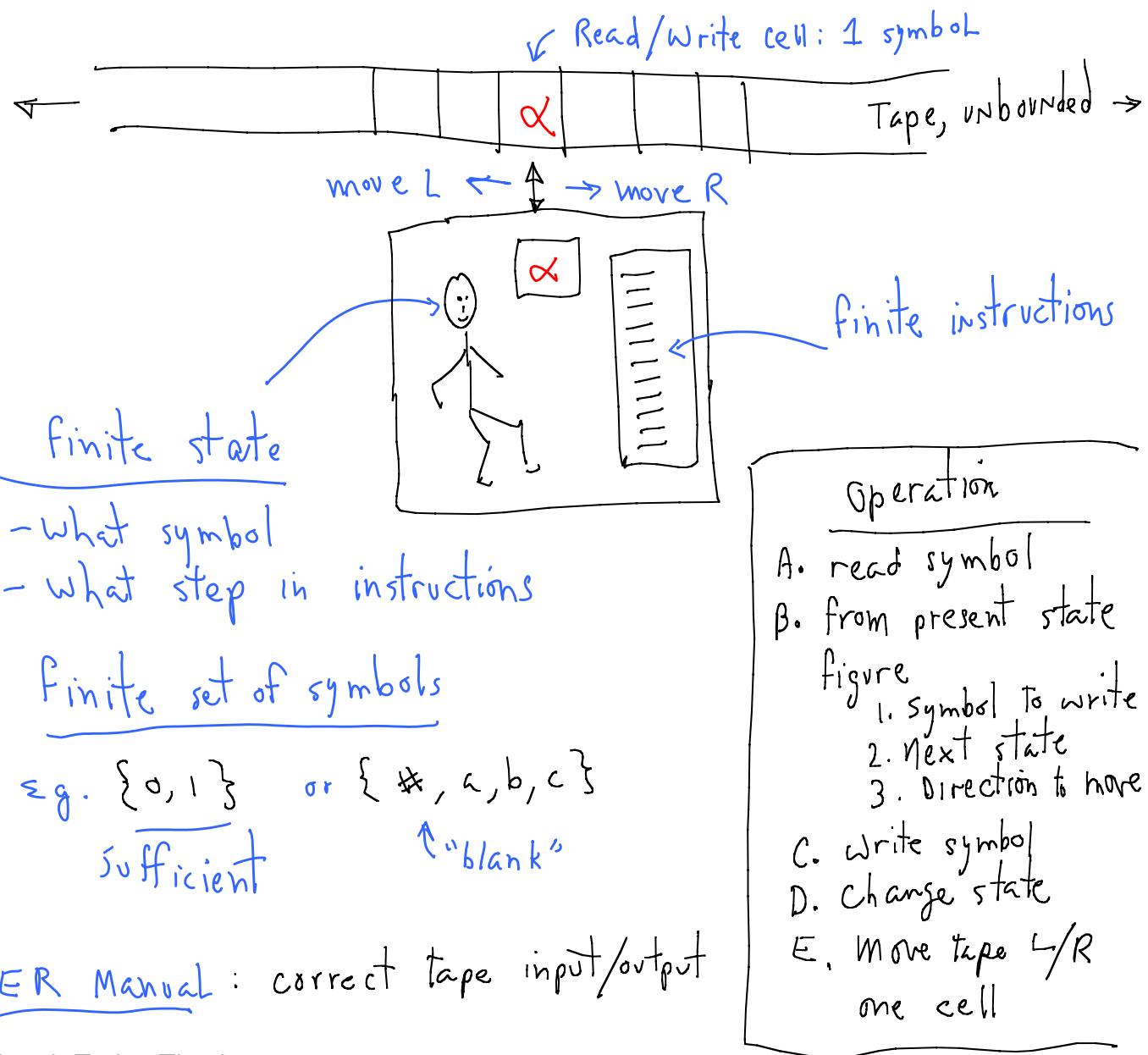
---- Answers questions that are interesting to us.

---- Symbolic computation: abstraction into symbols, manipulation of symbols.



BIG IDEA: Define computation (automatic procedure)

History: numbers, arithmetic, geometry (planets), abacus, Pascal, Babbage, others, ... Turing.



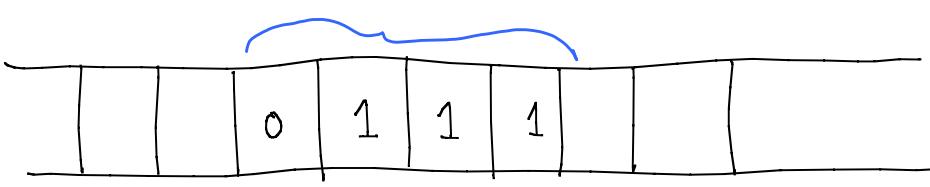
Church-Turing Thesis:

Any computation can be done by some Turing Machine (TM).

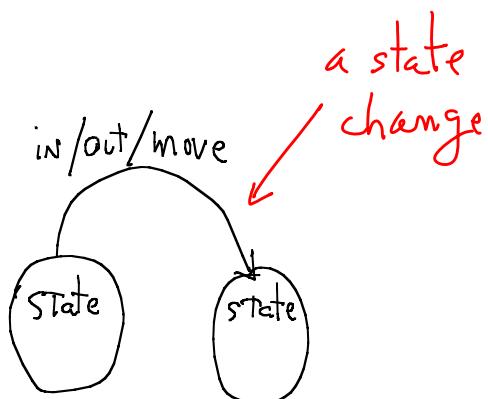
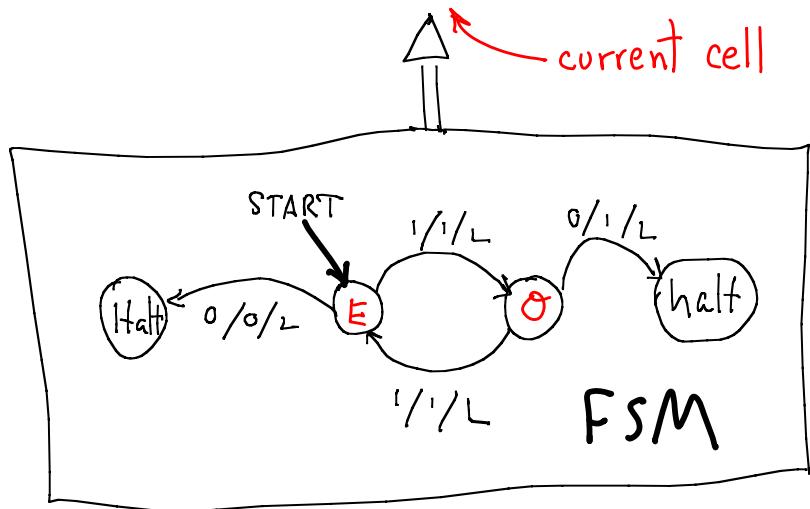
(Efficiently?)

Can't prove, but works so far.

State Transition diagrams



initial
Tape
configuration



Alternate representations of FSM:

=====
 (start state)
 (state, input) (output, move, next state)

=====
 (Even)
 (Even, 1) (1, L, Odd)
 (Even, 0) (0, L, Halt)
 (Odd, 1) (1, L, Even)
 (Odd, 0) (1, R, Halt)

=====
 A "unary" encoding of the above:

00100
 1 0 11 0 11 0 1 0 11 0 0
 1 0 1 0 1 0 1 0 111 0 0
 11 0 11 0 11 0 1 0 1 0 0 0
 11 0 1 0 11 0 11 0 111 0 0
 00

Alphabet, aka
Symbol Set
 $\Sigma = \{0, 1, *\}$

functional view

$$\text{state}^* = f(\text{state}, \text{in})$$

$$\text{out} = g(\text{state}, \text{in})$$

$$\text{move} = h(\text{state}, \text{in})$$

$i = \text{state}$

OR use per-state func.s

$$f_{\text{state}_i}(\text{in}) = \text{state}_j$$

$$g_{\text{state}_i}(\text{in}) = \text{output}$$

$$h_{\text{state}_i}(\text{in}) = \text{move}$$

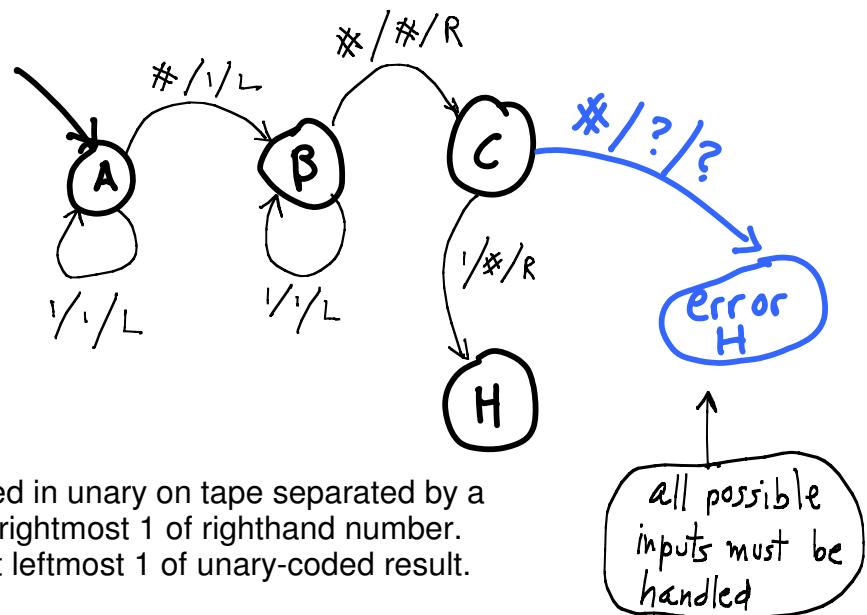
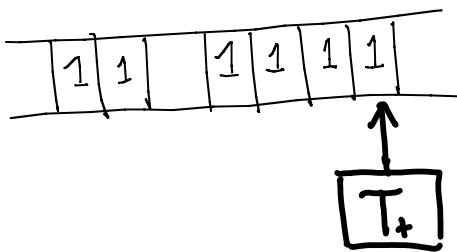
What does it do?

alternate
description
of
FSM

See
LC3
FSM inputs
& states

Unary Adder

T_+



User Manual: Start with two numbers coded in unary on tape separated by a single blank, and RW-head positioned on rightmost 1 of righthand number. Machine halts with RW-head positioned at leftmost 1 of unary-coded result.

Q. Does this work for input of 0 in one or both numbers?

Q. What is state A's purpose? B's? describe in words.

What else?

With a little more thought we can build:

- Tu-: A unary subtractor
- Tb+: A binary adder
- Tb-: A binary subtractor
- ...

But, this is boring, whenever I want to do something new, I have to build a new machine.

BIG IDEA: Make a TM simulator (call it UTM)

--- UTM simulates any other machine A, if we put a description of A on UTM's input tape and layout A's input tape in a simulated tape encoding on UTM's tape.

--- Turing demonstrated one, and a way of describing machines (see below).

- 1.a. Pattern match A's current state w/ current-state part of rule.
- 1.b. If match, go to 2; otherwise, advance to next rule and go to 1.
- 2.a. Find the current location of the simulated RW-head,
pattern match cell content with rule's input symbol.
- 2.b. If matched, go to 3. Otherwise, advance to next rule and go to 1.a.
3. Copy output symbol to current simulated tape cell.
4. Copy next-state symbol to current-state area.
5. Move simulated head as needed. Go to 1.

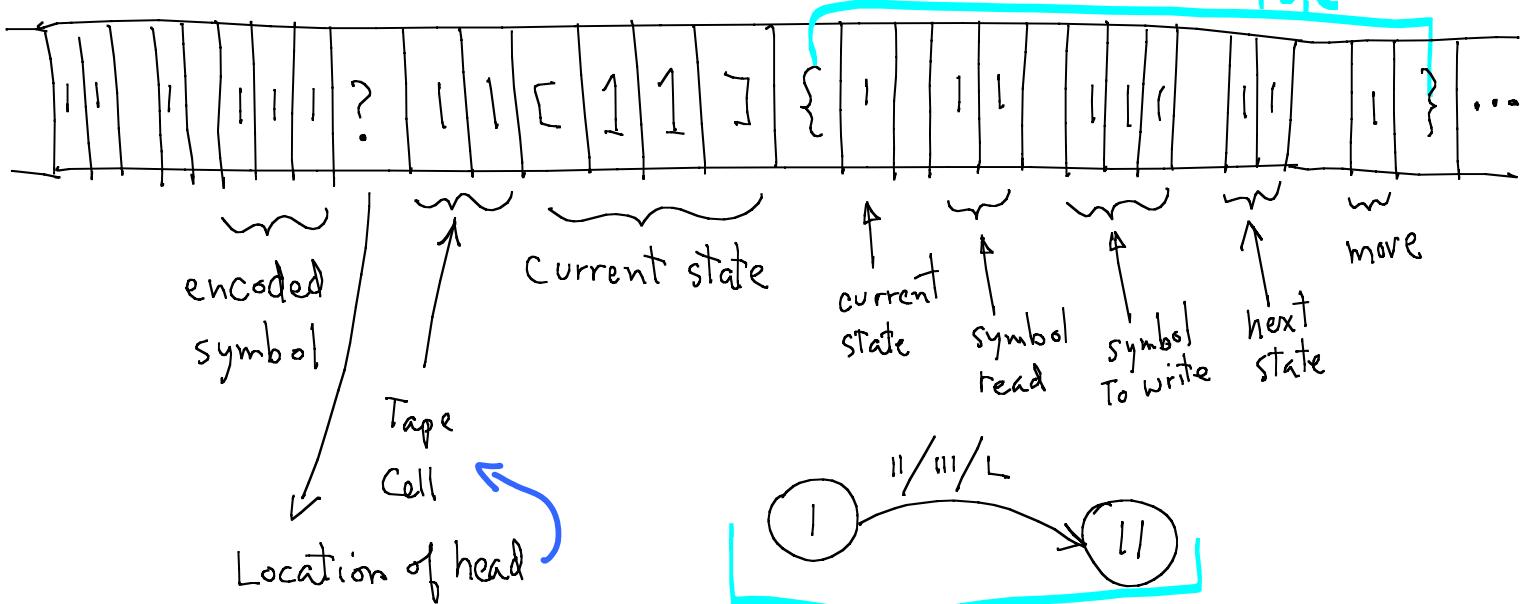
Built using some basic TMs: Tcopy, Tmatch, Tshifttape, ...

TM Description

How do we "describe" some M for UTM?

How many symbols do we need?
How many simulated machine
symbols, states?

rule

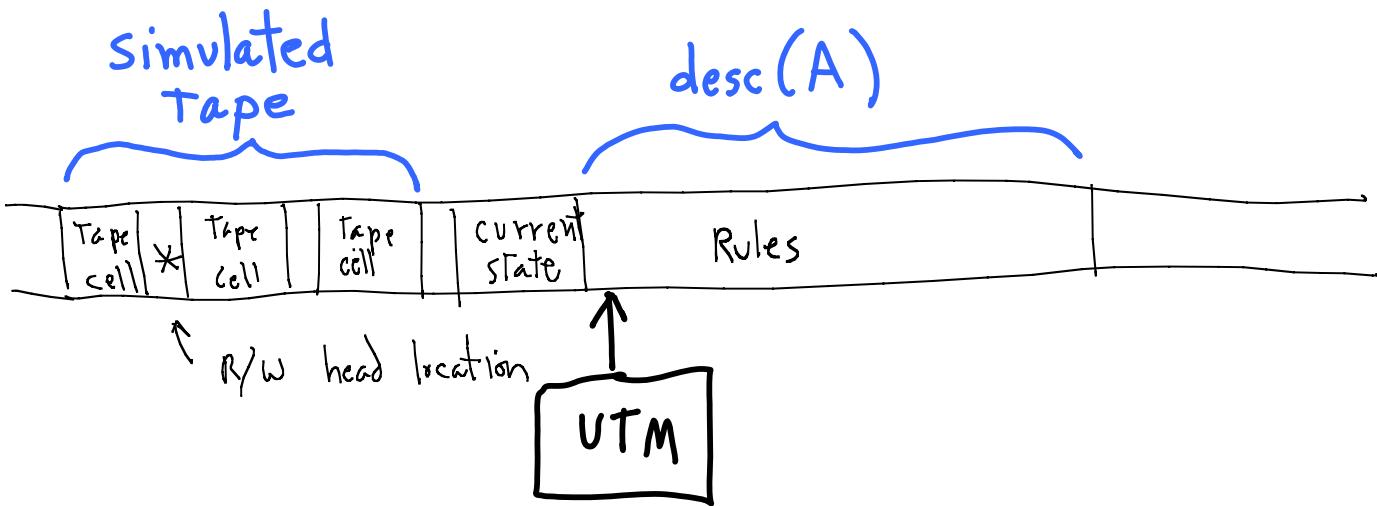


ENCODING

$$\begin{aligned} \Sigma &= \{a, b, c, \dots\} & S &= \{\text{START}, A, B, C, \dots\} \\ &\Rightarrow \{1, 11, 111, \dots\} & \Rightarrow \{1, 11, 111, 1111, \dots\} \end{aligned}$$

- Uses a **FIXED SYMBOL SET**
- BUT, Can encode any size symbol set.
Can encode characters, numbers, strings, images, ...
- Has **FIXED NUMBER OF STATES**,
- BUT, simulates machines w/ any number of states.
- Uses a **BOUNDED AREA OF TAPE**,
- BUT, relocates to expand simulated tape as needed.
- Uses **PATTERN MATCHING**, not states to determine a match between state or symbol codes.
(Using states to count would limit number of simulated symbols/states possible.)
- Any universal machine could be used, with larger symbol sets (say, binary integers), can encode more economically.

N bit integers $\Rightarrow 2^N$ symbols, 32-bit words $\Rightarrow 4G$ symbols



Programmability and Translation

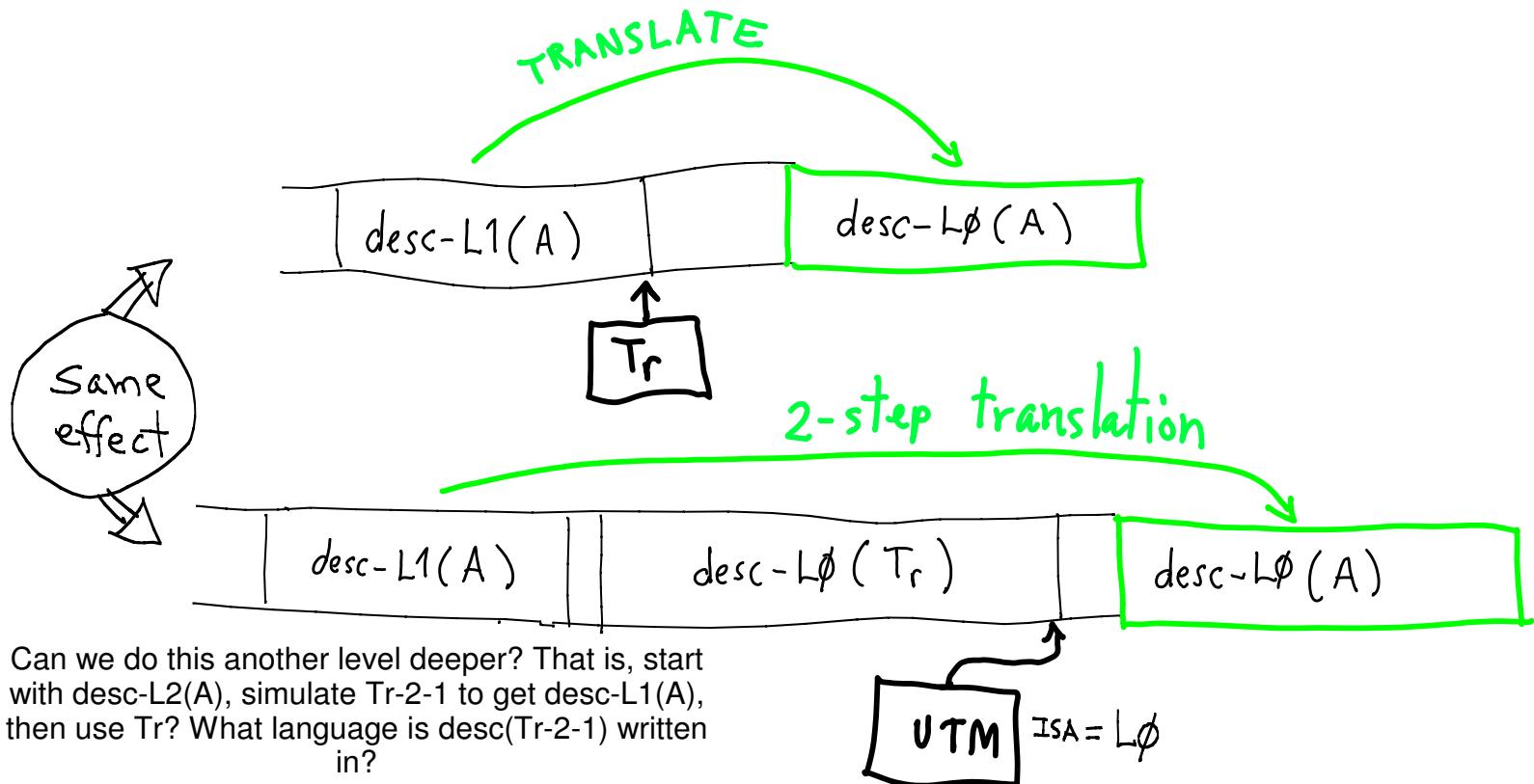
Can simulate any TM, no matter the size of its symbol set, the number of states, or the number of rules (UTM uses pattern matching, not counting via UTM states).

The description is written in the encoding for that UTM (there can be more than one UTM, each using a different encoding for machines/tape/symbols...)

The description language is the "machine language" for that UTM.

Q. Can we write a language translator that can be simulated by UTM?

--- Call UTM's machine language, L_0 . Suppose there is another language, L_1 , that can be used to describe TMs. Is there a translator machine T_{10} ?



Can we do this another level deeper? That is, start with $desc-L_2(A)$, simulate $Tr-2-1$ to get $desc-L_1(A)$, then use Tr ? What language is $desc(Tr-2-1)$ written in?

UTM $\text{ISA} = L_0$

BIG IDEA: machine descriptions as input data.

--- Translate between descriptions: C++ => C => ASM => machine language (ISA)

--- Ask questions about Algorithms/Procedures/TMs using desc(M):

Given machine M and input x, will xM ever halt? (read " xM " as "x operated on by M").

what about

- 2-way infinite tapes?
- 2d tapes?
- RAM tape?
- Multiple R/W heads?
- Alphabet (Symbol set)?

No difference in computational capability!
(maybe faster, that's all)

\Rightarrow 2 symbols (min) $\{0,1\}$ or $\{\star,0,1\}$

Why not use REALLY HUGE symbol sets?

32-bit word => 4 Giga-symbol (4 Billion)

64-bit word => 16 Exa-symbol (16 Billion Billion)

[See "The Myth of the Turing Machine", C. Eliasmith]

[See "Turing Machines", Stanford Encyclopedia of Philosophy]

