

A Crash Course on Fast Interactive Proofs

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Interactive Proofs

- Prover **P** and Verifier **V**.
- **P** solves problem, tells **V** the answer.
 - Then **P** and **V** have a conversation.
 - **P**'s goal: convince **V** the answer is correct.
- Requirements:
 - 1. Completeness: an honest **P** can convince **V** to accept.
 - 2. Soundness: **V** will catch a lying **P** with high probability.



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- Requirements:
 - 1. Completeness: an honest **P** can convince **V** to accept.
 - 2. Soundness: **V** will catch a lying **P** with high probability.
 - This must hold even if **P** is computationally unbounded and trying to trick **V** into accepting the incorrect answer.



Interactive Proof Techniques: Preliminaries

Schwartz-Zippel Lemma

- Recall **FACT:** Let $p \neq q$ be univariate polynomials over field F , of degree at most d . Then $\Pr_{r \in F}[p(r) = q(r)] \leq \frac{d}{|F|}$.

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- The **Schwartz-Zippel lemma** is a multivariate generalization:
 - Let $p \neq q$ be ℓ -variate polynomials over field F of total degree at most d . Then $\Pr_{r \in F^\ell}[p(r) = q(r)] \leq \frac{d}{|F|}$.

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 - “Total degree” refers to the maximum sum of degrees of all variables in any term. E.g., $x_1^2 x_2 + x_1 x_2$ has total degree 3.

Low-Degree and Multilinear Extensions

- Definition [**Extensions**]. Given a function $f: \{0,1\}^\ell \rightarrow \mathbf{F}$, a ℓ -variate polynomial g over $\mathbf{F}$ is said to **extend** f if $f(x) = g(x)$ for all $x \in \{0,1\}^\ell$.
- Definition [**Multilinear Extensions**]. Any function $f: \{0,1\}^\ell \rightarrow \mathbf{F}$ has a **unique** multilinear extension (MLE), denoted $\tilde{f}$.

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- Definition [**Multilinear Extensions**]. Any function $f: \{0,1\}^\ell \rightarrow \mathbf{F}$ has a **unique** multilinear extension (MLE), denoted $\tilde{f}$.
 - Multilinear means the polynomial has degree at most 1 in each variable.
 - $(1 - x_1)(1 - x_2)$ is multilinear, $x_1^2 x_2$ is not.

$$f : \{0,1\}^2 \rightarrow \mathbf{F}$$

	0	1
0	1	2
1	8	10

$$\tilde{f} : \mathbf{F}^2 \rightarrow \mathbf{F}$$

	0	1	2	3	4	5	
0	1	2	3	4	5	6	
1	8	10	12	14	16	18	
2	15	18	21	24	27	30	...
3	22	26	30	34	38	42	
4	29	34	39	44	49	56	
5	36	42	48	54	60	68	
			...				

Low-Degree and Multilinear Extensions

- Fact [VSBW13]: Given as input all 2^ℓ evaluations of a function $f: \{0,1\}^\ell \rightarrow \mathbf{F}$, for any point $r \in \mathbf{F}^\ell$ there is an $O(2^\ell)$ -time algorithm for evaluating $\tilde{f}(r)$.
 - Assuming field addition and multiplication operations take one time step.

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- The following slides prove a slightly weaker $O(\ell \cdot 2^\ell)$ time bound.

A Useful Expression for the MLE

- For any $w \in \{0,1\}^\ell$, define $\delta_w: \{0,1\}^\ell \rightarrow \mathbf{F}$ via:
$$\delta_w(w) = 1 \text{ and } \delta_w(y) = 0 \text{ for all } y \neq w.$$

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- Lemma (**Lagrange Interpolation**): Let $f: \{0,1\}^\ell \rightarrow \mathbf{F}$. Then as formal polynomials, $\tilde{f}(x) = \sum_{w \in \{0,1\}^\ell} f(w) \cdot \tilde{\delta}_w(x)$.
- Proof: The RHS is multilinear. The lemma then follows from Fact 1 and uniqueness of the MLE.

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- **Fact 2:** (Explicit Expression for Lagrange Basis Polynomials)

$$\tilde{\delta}_w(x) = \prod_{i=1}^{\ell} (x_i w_i + (1 - x_i)(1 - w_i)).$$

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 - For each $w \in \{0,1\}^\ell$, $\tilde{\delta}_w(r)$ can be computed with $O(\ell)$ field operations.
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 - $\tilde{\delta}_w(r) = \prod_{i=1}^\ell (r_i w_i + (1 - r_i)(1 - w_i))$.
- Can reduce to time $O(2^\ell)$ via dynamic programming.

The Sum-Check Protocol [LFKN90]



Sum-Check Protocol [LFKN90]

- Input: V given oracle access to a ℓ -variate polynomial g over field $\mathbf{F}$.
- Goal: compute the quantity:

$$\sum_{b_1 \in \{0,1\}} \sum_{b_2 \in \{0,1\}} \dots \sum_{b_\ell \in \{0,1\}} g(b_1, \dots, b_\ell).$$

- **Start:** **P** sends claimed answer $\mathcal{C}_1$. The protocol must check that:

$$\mathcal{C}_1 = \sum_{b_1 \in \{0,1\}} \sum_{b_2 \in \{0,1\}} \dots \sum_{b_\ell \in \{0,1\}} g(b_1, \dots, b_\ell).$$

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- If this check passes, it is safe for **V** to believe that C_1 is the correct answer, so long as **V** believes that $s_1 = H_1$.
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- How to check this? Just check that s_1 and H_1 agree at a random point r_1 .
- **V** can compute $s_1(r_1)$ directly from **P**'s first message, but not $H_1(r_1)$.

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- **Round ℓ (Final round):** **P** sends univariate polynomial $s_\ell(X_\ell)$ claimed to equal

$$H_\ell := g(r_1, \dots, r_{\ell-1}, X_\ell).$$

- **V** checks that $s_{\ell-1}(r_{\ell-1}) = s_\ell(0) + s_\ell(1)$.
- **V** picks r_ℓ at random, and needs to check that $s_\ell(r_\ell) = g(r_1, \dots, r_\ell)$.
 - No need for more rounds. **V** can perform this check with one oracle query.

Analysis of the Sum-Check Protocol

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- Completeness holds by design: If **P** sends the prescribed messages, then all of **V**'s checks will pass.

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- Completeness holds by design: If **P** sends the prescribed messages, then all of **V**'s checks will pass.
- Soundness: If **P** does not send the prescribed messages, then **V** rejects with probability at least $1 - \frac{\ell \cdot d}{|F|}$, where d is the maximum degree of g in any variable.
- Proof is by induction on the number of variables ℓ .

Costs of the Sum-Check Protocol

- Total communication is $O(d\ell)$ field elements.
 - P sends ℓ messages, each a univariate polynomial of degree at most d . V sends $\ell - 1$ messages, each consisting of one field element.

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- P 's runtime is at most:
 $O(d \cdot 2^\ell \cdot [\text{time required to evaluate } g \text{ at one point}])$.

An Application of the Sum-Check Protocol

A Doubly-Efficient Interactive Proof for
Matrix Multiplication

[Thaler13]: Optimal IP For $n \times n$ MatMult

- Goal: Given $n \times n$ input matrices A, B over field $\mathbf{F}$, compute $C = A \cdot B$.

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Problem Size	Naïve MatMult Time	Additional $\mathbf{P}$ time	$\mathbf{V}$ Time	Rounds	Protocol Comm
1024 x 1024	2.17 s	0.03 s	0.09 s	11	264 bytes
2048 x 2048	18.23 s	0.13 s	0.30 s	12	288 bytes

Comparison to Freivalds' Algorithm

- Freivalds (MFCS, 1979) gave the following protocol for MatMult. To check $A \cdot B = D$:
 - V picks random vector x .
 - Accepts if $A \cdot (Bx) = Dx$.
 - **No** extra work for P , $O(n^2)$ time for V .

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 - Accepts if $A \cdot (Bx) = Dx$.
 - **No** extra work for P , $O(n^2)$ time for V .
- Our big win: verifying algorithms that invoke MatMult, but aren't really interested in matrices.
 - E.g., Best-known subgraph-counting algorithms square the adjacency matrix, but are only interested in a single number.
 - Total communication for us is $O(\log n)$, Freivalds' is $\Omega(n^2)$.

MatMult Protocol: Technical Details

Notation

- Given $n \times n$ input matrices A, B over field $\mathbf{F}$, interpret A and B as functions mapping $\{0,1\}^{\log n} \times \{0,1\}^{\log n}$ to $\mathbf{F}$ via $A(i_1, \dots, i_{\log n}, j_1, \dots, j_{\log n}) = A_{ij}$.
- Let $C = A \cdot B$ denote the true answer.
- Let $\tilde{A}, \tilde{B}$ denote the multilinear extensions of the functions A and B .

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$$C(\mathbf{i}, \mathbf{j}) = \sum_{\mathbf{k} \in \{0,1\}^{\log n}} A(\mathbf{i}, \mathbf{k}) B(\mathbf{k}, \mathbf{j})$$

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- So $\mathcal{V}$ applies sum-check protocol to compute

$$\tilde{C}(\mathbf{r}_1, \mathbf{r}_2) = \sum_{b_1 \in \{0,1\}} \cdots \sum_{b_{\log n} \in \{0,1\}} g(b_1, \dots, b_{\log n}),$$

where:

$$g(\mathbf{z}) := \tilde{A}(\mathbf{r}_1, \mathbf{z}) \tilde{B}(\mathbf{z}, \mathbf{r}_2).$$

Making V Fast

- At end of sum-check, V must evaluate

$$g(\mathbf{r}_3) = \tilde{A}(\mathbf{r}_1, \mathbf{r}_3) \tilde{B}(\mathbf{r}_3, \mathbf{r}_2).$$

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- Can be done in $O(n^2)$ time by “Fast Evaluation of MLE” lemma in preliminaries.

Making **P** Fast: A First Attempt

- Recall: we're using sum-check to compute

$$\sum_{b_1 \in \{0,1\}} \cdots \sum_{b_{\log n} \in \{0,1\}} g(b_1, \dots, b_{\log n}).$$

- Round i : **P** sends quadratic polynomial $s_i(X_i)$ claimed to equal:

$$\sum_{b_{i+1} \in \{0,1\}} \cdots \sum_{b_{\log n} \in \{0,1\}} g(r_{3,1}, \dots, r_{3,i-1}, X_i, b_{i+1}, \dots, b_{\log n})$$

- Suffices for **P** to specify $s_i(0), s_i(1), s_i(2)$

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- Round i : **P** sends quadratic polynomial $s_i(X_i)$ claimed to equal:

$$\sum_{b_{i+1} \in \{0,1\}} \cdots \sum_{b_{\log n} \in \{0,1\}} g(r_{3,1}, \dots, r_{3,i-1}, X_i, b_{i+1}, \dots, b_{\log n})$$

- Suffices for **P** to specify $s_i(0), s_i(1), s_i(2)$

- Thus: **Enough for **P** to evaluate g at all points of the form**

$$(r_{3,1}, \dots, r_{3,i-1}, \{0,1,2\}, b_{i+1}, \dots, b_{\log n}): b_{i+1}, \dots, b_{\log n} \in \{0,1\}^{\log n - i}$$

Making **P** Fast: A First Attempt

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- This is $O(\frac{n}{2^i})$ points.

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- This is $O\left(\frac{n}{2^i}\right)$ points.

- Recall $\tilde{A}$ and $\tilde{B}$ can each be evaluated at any input in $O(n^2)$ time, and hence so can g .

- So **P** can compute s_i in $O\left(\frac{n}{2^i} \cdot n^2\right) = O(n^3/2^i)$ time.

Making **P** Fast: A First Attempt

- Recall: we're using sum-check to compute $\sum_{b_1 \in \{0,1\}} \cdots \sum_{b_{\log n} \in \{0,1\}} g(b_1, \dots, b_{\log n})$.
- Round i : **P** sends quadratic polynomial $s_i(X_i)$ claimed to equal:
 $\sum_{b_{i+1} \in \{0,1\}} \cdots \sum_{b_{\log n} \in \{0,1\}} g(r_{3,1}, \dots, r_{3,i-1}, X_i, b_{i+1}, \dots, b_{\log n})$
 - Suffices for **P** to specify $s_i(0), s_i(1), s_i(2)$
 - Thus: **Enough for P to evaluate g at all points of the form**
 $(r_{3,1}, \dots, r_{3,i-1}, \{0,1,2\}, b_{i+1}, \dots, b_{\log n}): b_{i+1}, \dots, b_{\log n} \in \{0,1\}^{\log n - i}$
 - This is $O(\frac{n}{2^i})$ points.
 - Recall $\tilde{A}$ and $\tilde{B}$ can each be evaluated at any input in $O(n^2)$ time, and hence so can g .
- Over **all** rounds, this is $O(\sum_i n^3 / 2^i) = O(n^3)$ total time.

Making **P** Fast: Second Attempt

- **Recall:** Enough to evaluate g at all points of the form:
 $\mathbf{z} = (r_{3,1}, \dots, r_{3,i-1}, \{0,1,2\}, b_{i+1}, \dots, b_{\log n}): b_{i+1}, \dots, b_{\log n} \in \{0,1\}^{\log n - i}$
- Already showed: how to do this in $O(n^3/2^i)$ time.
- Can we improve this to $O(n^2)$ time?

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- **Recall: Enough to evaluate g at all points of the form:**
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- Already showed: how to do this in $O(n^3/2^i)$ time.
- Can we improve this to $O(n^2)$ time?
- Key observation each entry A_{ij} contributes to $\tilde{A}(\mathbf{r}_1, \mathbf{z})$ for less than 3 tuples $\mathbf{z}$ of the above form.
 - Similarly entry B_{ij} contributes to $\tilde{B}(\mathbf{z}, \mathbf{r}_2)$ for less than 3 tuples $\mathbf{z}$ of the above form.

Making **P** Fast: Second Attempt

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 - Recall: $\tilde{A}(\mathbf{r}_1, \mathbf{z}) = \sum_{(i,j) \in \{0,1\}^{2 \log n}} A(i,j) \cdot \tilde{\delta}_{(i,j)}(\mathbf{r}_1, \mathbf{z})$, where

$$\tilde{\delta}_{(i,j)}(\mathbf{r}_1, \mathbf{z}) = \tilde{\delta}_i(\mathbf{r}_1) \tilde{\delta}_j(\mathbf{z}) = \tilde{\delta}_i(\mathbf{r}_1) \cdot \prod_{t=1}^{\ell} (j_t z_t + (1 - j_t)(1 - z_t)).$$

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 - For $t \geq i + 1$, the t 'th entry of $\mathbf{z}$ is $b_t \in \{0,1\}$.
 - If $b_t \neq j_t$, then $\tilde{\delta}_j(\mathbf{z}) = 0$, so $\tilde{\delta}_{(i,j)}(\mathbf{r}_1, \mathbf{z}) = 0$.

Making **P** Fast: Second Attempt

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 - For $t \geq i + 1$, the t 'th entry of $\mathbf{z}$ is $b_t \in \{0,1\}$.
 - If $b_t \neq j_t$, then $\tilde{\delta}_j(\mathbf{z}) = 0$, so $\tilde{\delta}_{(i,j)}(\mathbf{r}_1, \mathbf{z}) = 0$.
 - i.e., $A(i,j)$ only contributes to $\tilde{A}(\mathbf{r}_1, \mathbf{z})$ if $(j_{i+1}, \dots, j_{\log n}) = (b_{i+1}, \dots, b_{\log n})$

Making **P** Fast: Second Attempt

- Summary: In round i , **P** must evaluate g at $O(\frac{n}{2^i})$ points of a special form (trailing entries are **Boolean**).
- Each matrix entry A_{ij}, B_{ij} contributes to only **at most three** of these evaluations.
- So **P** can run in $O(n^2)$ time per round, or $O(n^2 \log n)$ time across all $O(\log n)$ rounds.

Making **P** Fast: Third Attempt

- With care: can bring **P**'s time down to $O(n^2)$ across **all** rounds.
- Key idea: **Reuse work** across rounds.
 - If two matrix index pairs (i, j) and (i', j') in $\{0, 1\}^{\log n} \times \{0, 1\}^{\log n}$ agree in their last k bits, then A_{ij} and $A_{i'j'}$ contribute to the **same** points $\mathbf{z}$ in rounds k and up.
 - Can treat (i, j) and (i', j') as a single entity thereafter.
 - Only $O(n/2^k)$, entities of interest in round k .
 - Total work across all rounds is proportional to

$$\sum_{1 \leq k \leq \log n} n / 2^k = 2n.$$

Details of Third Attempt

- $\tilde{A}(\mathbf{x}_1, \mathbf{x}_2) = \sum_{(i,j) \in \{0,1\}^{2 \log n}} A(i,j) \cdot \tilde{\delta}_{(i,j)}(\mathbf{x}_1, \mathbf{x}_2)$

where $\tilde{\delta}_w(r) = \prod_{i=1}^{\ell} (r_i w_i + (1 - r_i)(1 - w_i))$.

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- Consider two indices $(\mathbf{i}, \mathbf{j})$ and $(\mathbf{i}', \mathbf{j}')$ that differ in only their first bit, e.g., $(\mathbf{i}, \mathbf{j}) = 00 \dots 0$ and $(\mathbf{i}', \mathbf{j}') = 10 \dots 0$.

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- Then the products defining $\tilde{\delta}_{(\mathbf{i}, \mathbf{j})}$ and $\tilde{\delta}_{(\mathbf{i}', \mathbf{j}')}$ are the same **except for the first term**.
 - For $\tilde{\delta}_{(\mathbf{i}, \mathbf{j})}(\mathbf{x}_1, \mathbf{x}_2)$ the first term is $x_{1,1} \cdot 0 + (1 - x_{1,1}) \cdot 1 = (1 - x_{1,1})$.
 - For $\tilde{\delta}_{(\mathbf{i}', \mathbf{j}')}(\mathbf{x}_1, \mathbf{x}_2)$ the first term is $x_{1,1} \cdot 1 + (1 - x_{1,1}) \cdot 0 = x_{1,1}$.

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 - For $\tilde{\delta}_{(\mathbf{i}', \mathbf{j}')}(\mathbf{x}_1, \mathbf{x}_2)$ the first term is $x_{1,1} \cdot 1 + (1 - x_{1,1}) \cdot 0 = x_{1,1}$.
- Once the **first variable** $x_{1,1}$ is bound to a fixed value $r_{1,1}$ by the MatMult protocol, this difference is fixed.
 - i.e., no need for **P** to remember both $A(\mathbf{i}, \mathbf{j})$ and $A(\mathbf{i}', \mathbf{j}')$.
 - Suffices just to remember $(1 - r_{1,1})A(\mathbf{i}, \mathbf{j}) + r_{1,1}A(\mathbf{i}', \mathbf{j}')$.

Details of Third Attempt

$A(0,0,0)$	$A(0,0,1)$	$A(0,1,0)$	$A(0,1,1)$	$A(1,0,0)$	$A(1,0,1)$	$A(1,1,0)$	$A(1,1,1)$
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$(1 - r_1) A(0,0,0)$ $+ r_1 A(1,0,0)$ $:= B(0,0)$	$(1 - r_1) A(0,0,1)$ $+ r_1 A(1,0,1)$ $:= B(0,1)$	$(1 - r_1) A(0,1,0)$ $+ r_1 A(1,1,0)$ $:= B(1,0)$	$(1 - r_1) A(0,1,1)$ $+ r_1 A(1,1,1)$ $:= B(1,1)$
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$(1 - r_2) B(0,0) + r_2 B(1,0)$ $:= C(0)$	$(1 - r_2) B(0,1) + r_2 B(1,1)$ $:= C(1)$
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$(1 - r_3) C(0) + r_3 C(1)$

A Second Application of the Sum-Check Protocol

A Doubly-Efficient Interactive Proof for
Counting Triangles

Counting Triangles

- Input: $A \in \{0,1\}^{n \times n}$, representing the adjacency matrix of a graph.
- Desired Output:
$$\frac{1}{6} \cdot \sum_{(i,j,k) \in [n]^3} A_{ij} A_{jk} A_{ki}$$
$$= \frac{1}{6} \cdot \sum_{(i,j) \in [n]^2} (A^2)_{ij} \cdot A_{ij}.$$

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$$= \frac{1}{6} \cdot \sum_{(i,j) \in [n]^2} (A^2)_{ij} \cdot A_{ij}.$$
- View A and A^2 as functions mapping $\{0,1\}^{\log n} \times \{0,1\}^{\log n}$ to $\mathbf{F}$.
- Define the polynomial $h(X, Y) = \widetilde{(A^2)}(X, Y) \tilde{A}(X, Y)$.

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- The Protocol:
 - Apply the sum-check protocol to h .

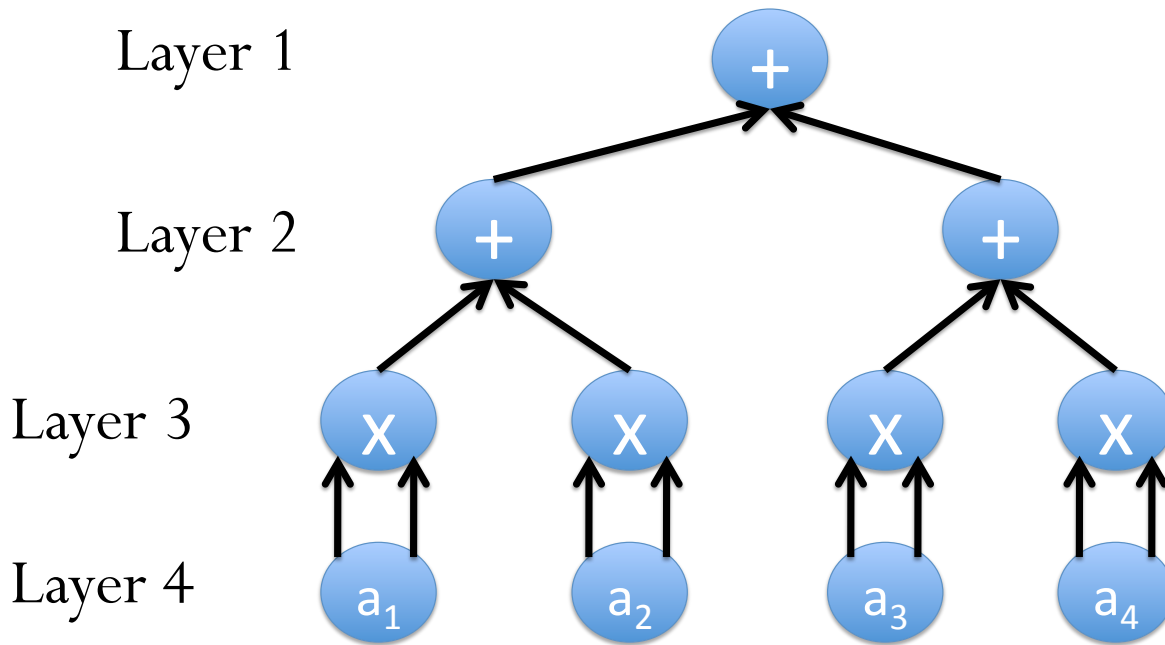
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- Define the polynomial $h(X, Y) = \widetilde{(A^2)}(X, Y) \tilde{A}(X, Y)$.
- The Protocol:
 - Apply the sum-check protocol to h .
 - At the end of the protocol, $\mathbf{V}$ needs to evaluate:
$$h(r_1, r_2) = \widetilde{(A^2)}(r_1, r_2) \tilde{A}(r_1, r_2).$$
 - $\mathbf{V}$ can evaluate $\tilde{A}(r_1, r_2)$ on its own in $O(n^2)$ time. $\mathbf{V}$ uses the MatMult protocol to force $\mathbf{P}$ to compute $\widetilde{(A^2)}(r_1, r_2)$ for her.

The GKR Protocol

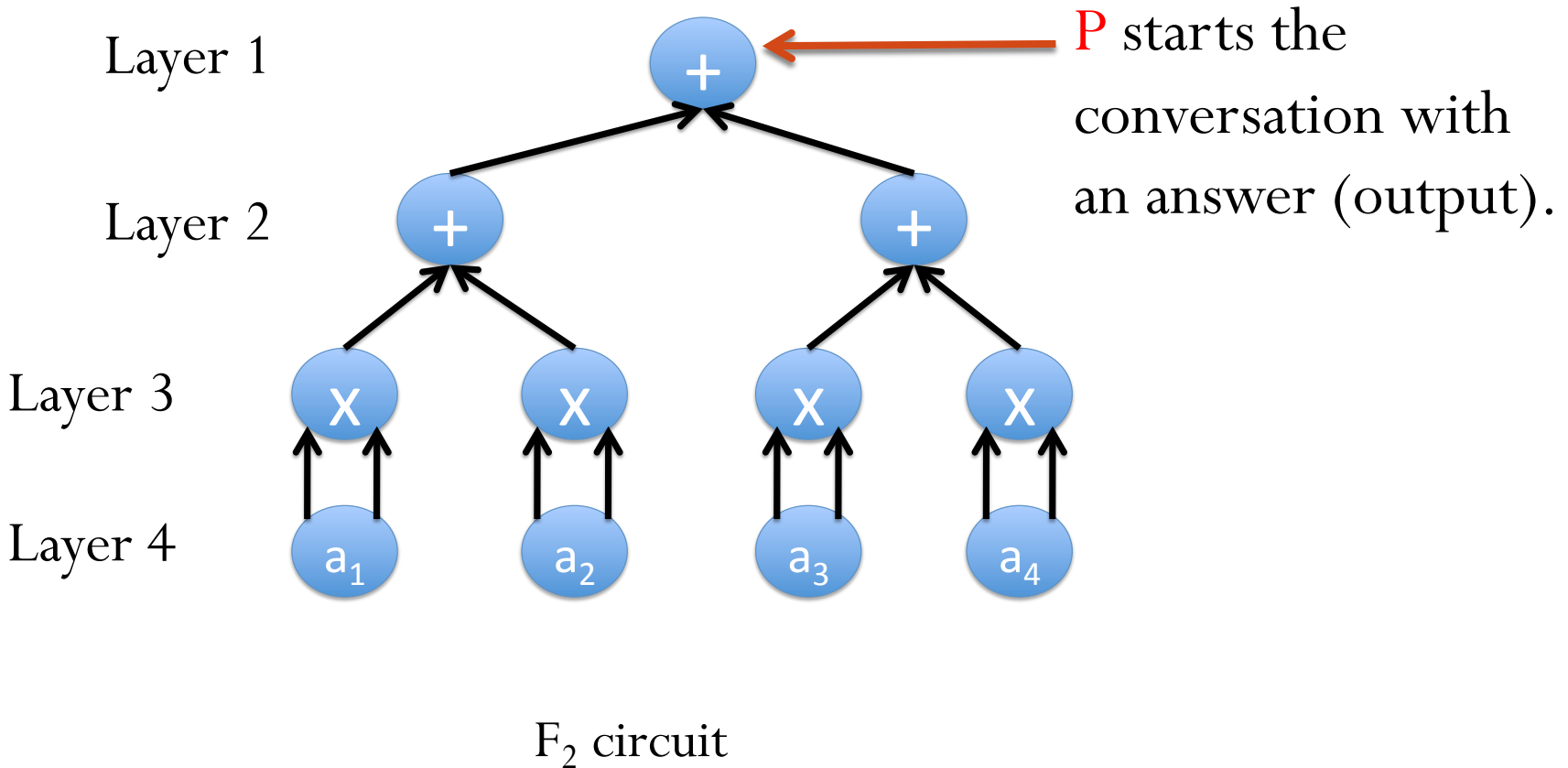
A General-Purpose Doubly-Efficient
Interactive Proof

The GKR Protocol: Overview

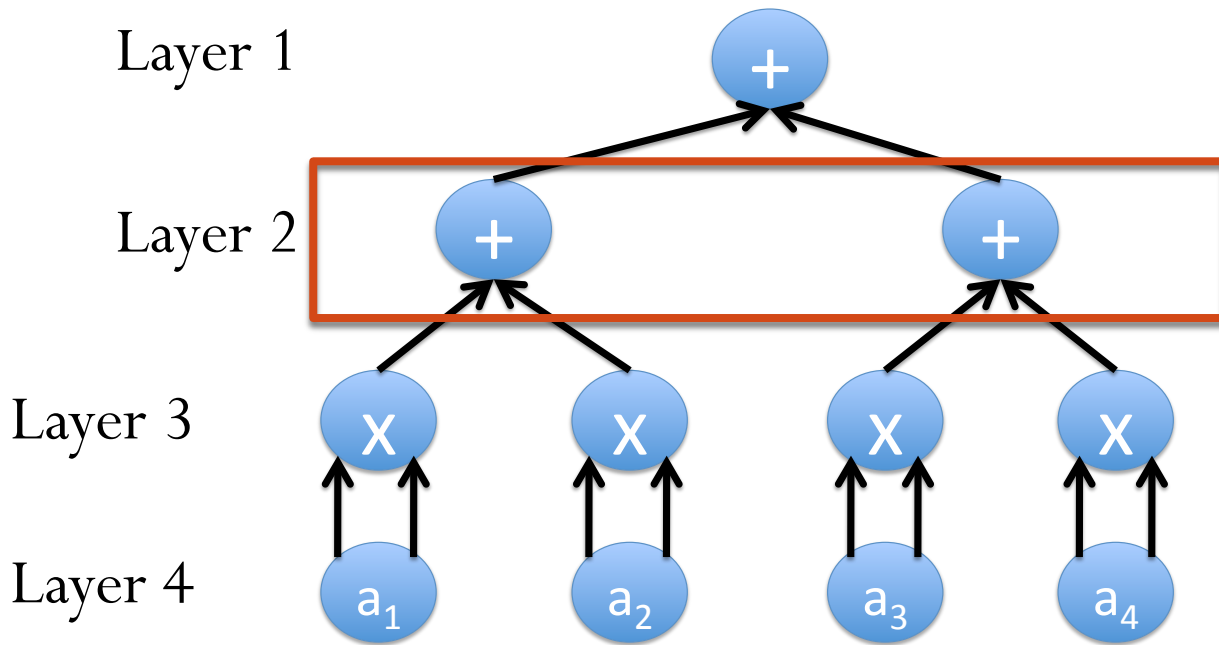


F_2 circuit

The GKR Protocol: Overview



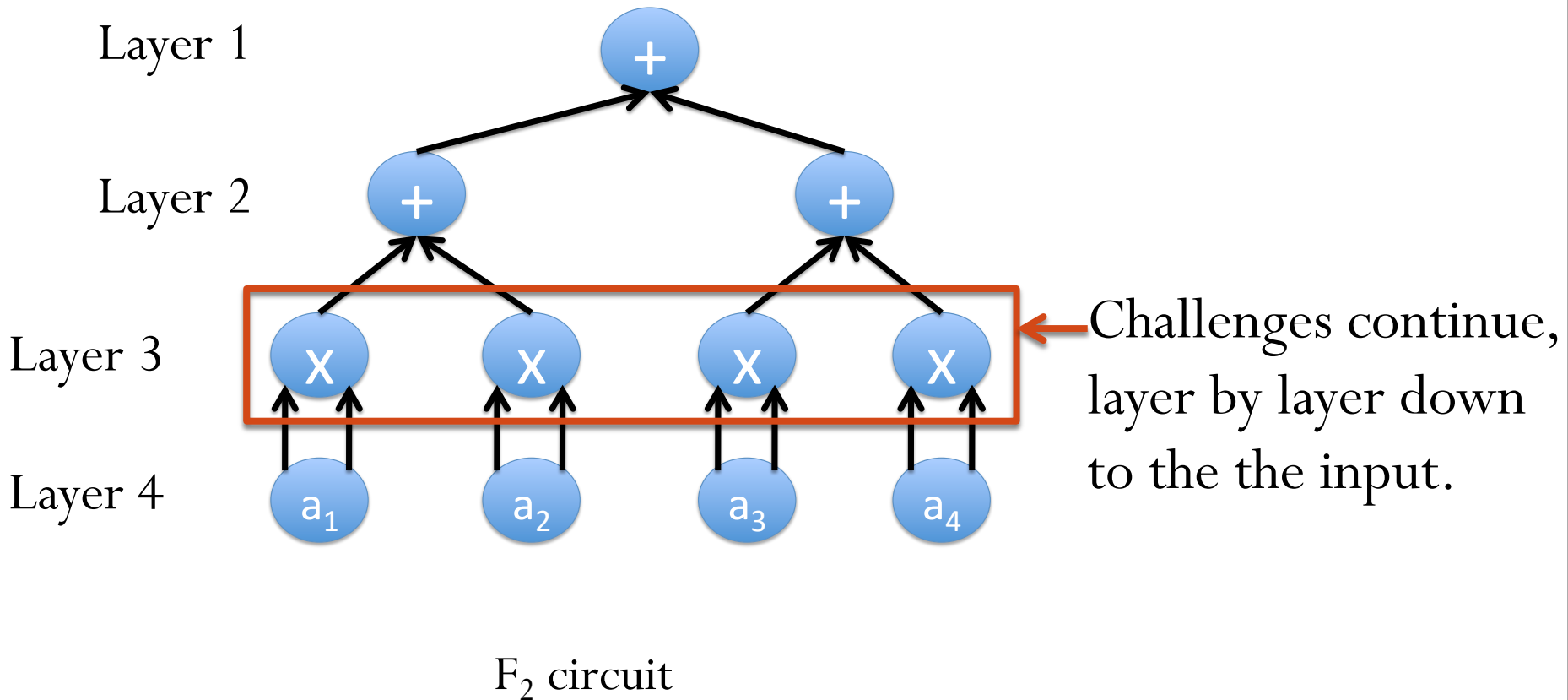
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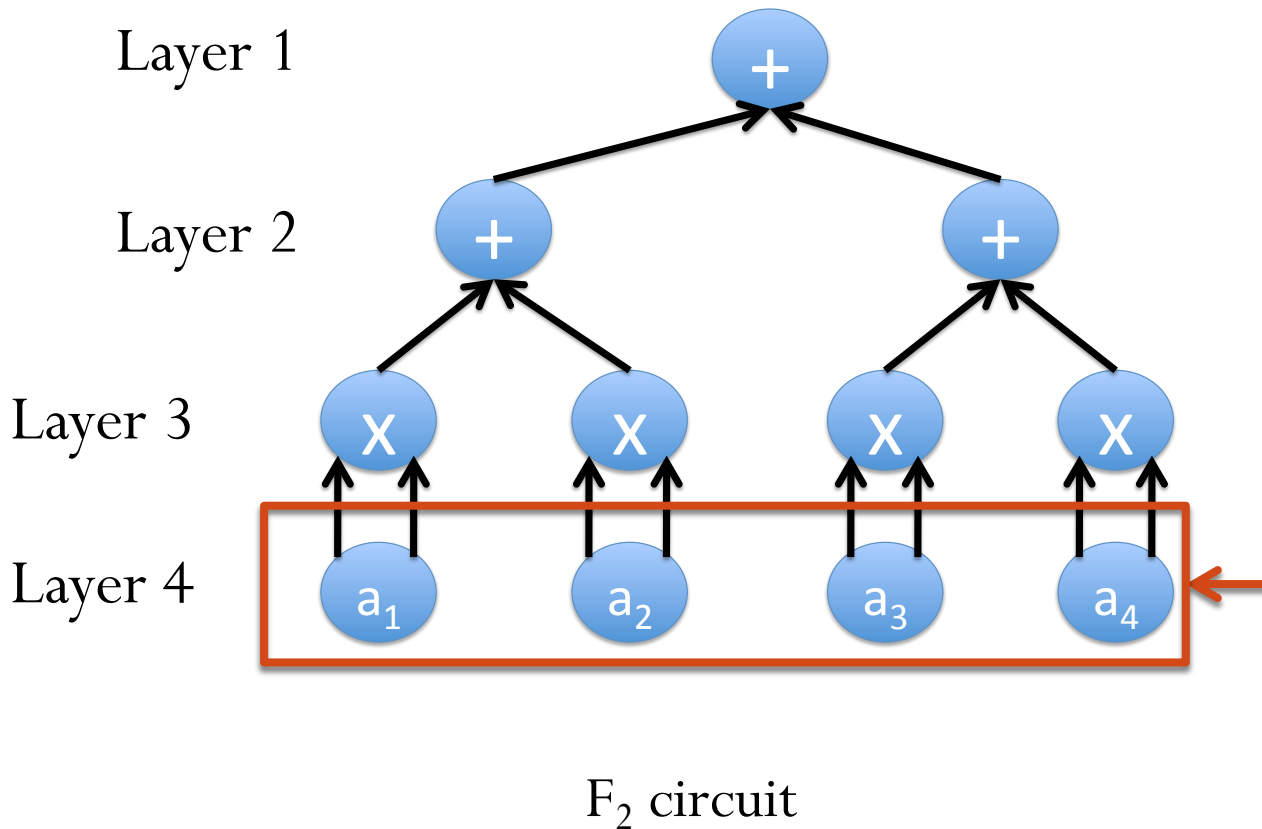
V sends series of challenges. P responds with info about next circuit level.

F_2 circuit

The GKR Protocol: Overview



The GKR Protocol: Overview



Finally, **P** says something about the (multilinear extension of the) input.

Notation

- Assume layers i and $i + 1$ of $\mathcal{C}$ have S gates each.
 - Assign each gate a binary label ($\log S$ bits).
- Let $W_i(\mathbf{a}): \{0,1\}^{\log S} \rightarrow \mathbf{F}$ output the value of gate $\mathbf{a}$ at layer i .

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- Let $\text{add}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}): \{0,1\}^{3 \log S} \rightarrow \mathbf{F}$ output 1 iff $(\mathbf{b}, \mathbf{c}) = (\text{in}_1(\mathbf{a}), \text{in}_2(\mathbf{a}))$ and gate $\mathbf{a}$ at layer i is an addition gate.

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- Let $\text{mult}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}): \{0,1\}^{3 \log S} \rightarrow \mathbf{F}$ output 1 iff $(\mathbf{b}, \mathbf{c}) = (\text{in}_1(\mathbf{a}), \text{in}_2(\mathbf{a}))$ and gate $\mathbf{a}$ at layer i is a multiplication gate.

GKR Protocol: Goal of Iteration i

- Iteration i starts with a claim from $\mathbf{P}$ about $\tilde{W}_i(\mathbf{r}_1)$ for a random point $\mathbf{r}_1 \in \mathbf{F}^{\log S}$.
- Goal: Reduce this to a claim about $\tilde{W}_{i+1}(\mathbf{r}_2)$ for a random point $\mathbf{r}_2 \in \mathbf{F}^{\log S}$.

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- Observation: $W_i(\mathbf{a}) =$

$$\sum_{\mathbf{b}, \mathbf{c} \in \{0,1\}^{\log S}} [\text{add}_i(\mathbf{a}, \mathbf{b}, \mathbf{c})(W_{i+1}(\mathbf{b}) + W_{i+1}(\mathbf{c})) + \text{mult}_i(\mathbf{a}, \mathbf{b}, \mathbf{c})(W_{i+1}(\mathbf{b}) \cdot W_{i+1}(\mathbf{c}))]$$

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- Hence, the following equality holds as formal polynomials:

$$\tilde{W}_i(\mathbf{a}) =$$

$$\sum_{\mathbf{b}, \mathbf{c} \in \{0,1\}^{\log s}} [\widetilde{\text{add}}_i(\mathbf{a}, \mathbf{b}, \mathbf{c})(\tilde{W}_{i+1}(\mathbf{b}) + \tilde{W}_{i+1}(\mathbf{c})) + \widetilde{\text{mult}}_i(\mathbf{a}, \mathbf{b}, \mathbf{c})(\tilde{W}_{i+1}(\mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{c}))]$$

GKR Protocol: Goal of Iteration i

- So V applies sum-check protocol to compute

- $\tilde{W}_i(\mathbf{r}_1) = \sum_{\mathbf{b}, \mathbf{c} \in \{0,1\}^{\log s}} g(\mathbf{b}, \mathbf{c})$, where:

$$\begin{aligned} g(\mathbf{b}, \mathbf{c}) = & \widetilde{\text{add}}_i(\mathbf{r}_1, \mathbf{b}, \mathbf{c})(\tilde{W}_{i+1}(\mathbf{b}) + \tilde{W}_{i+1}(\mathbf{c})) \\ & + \widetilde{\text{mult}}_i(\mathbf{r}_1, \mathbf{b}, \mathbf{c})(\tilde{W}_{i+1}(\mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{c})) \end{aligned}$$

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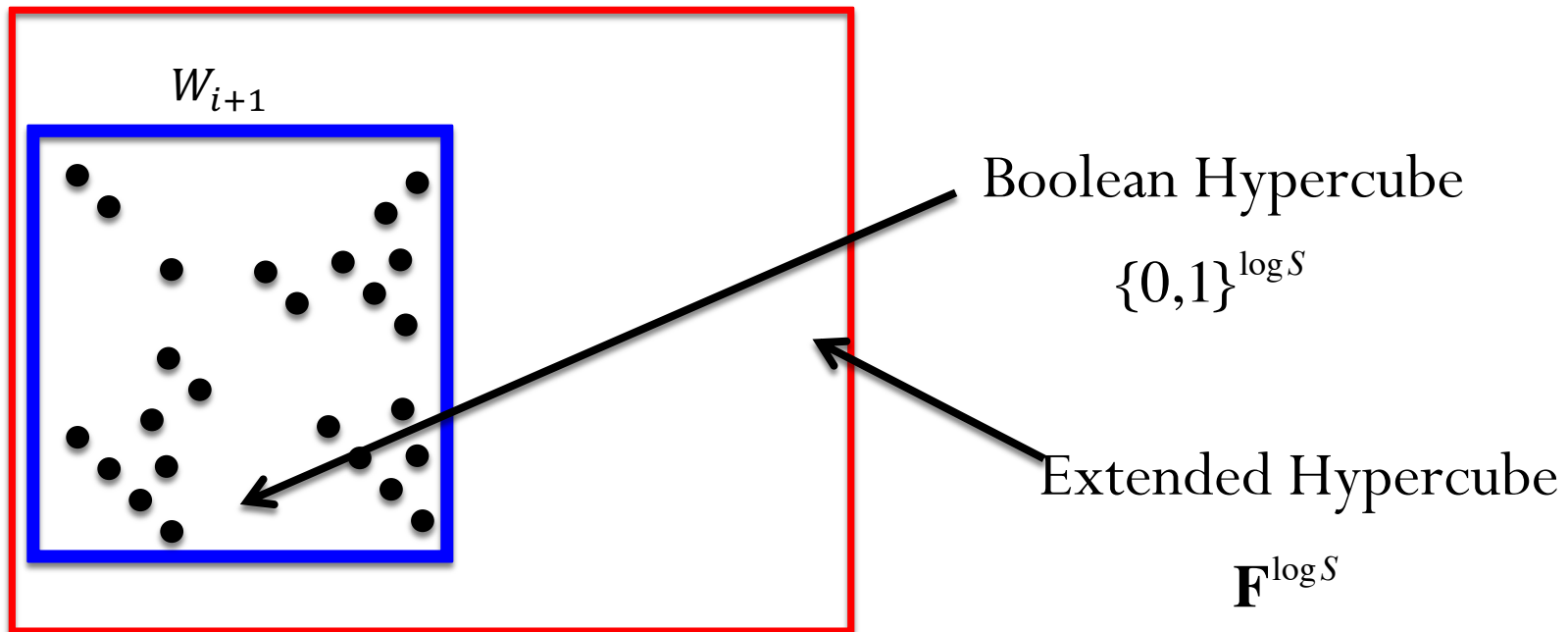
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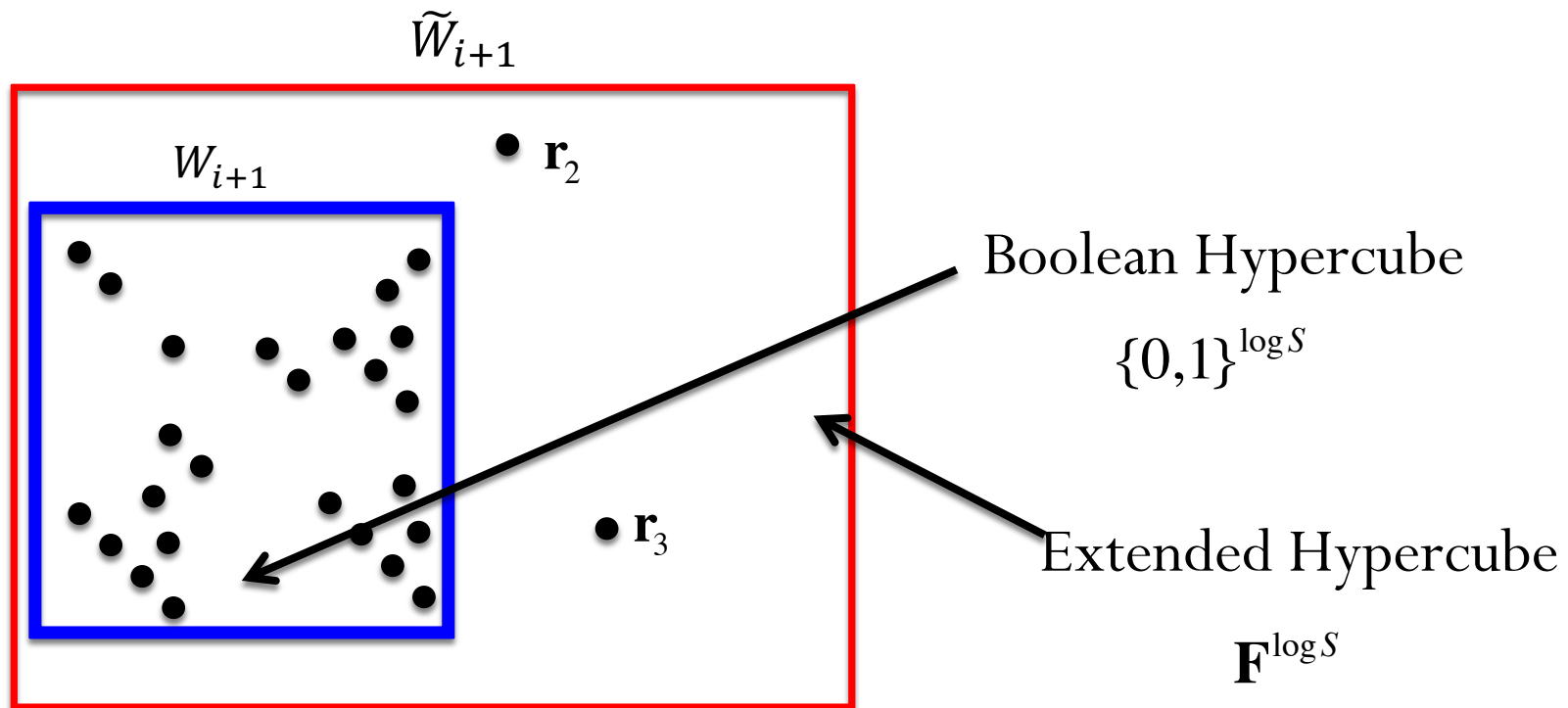
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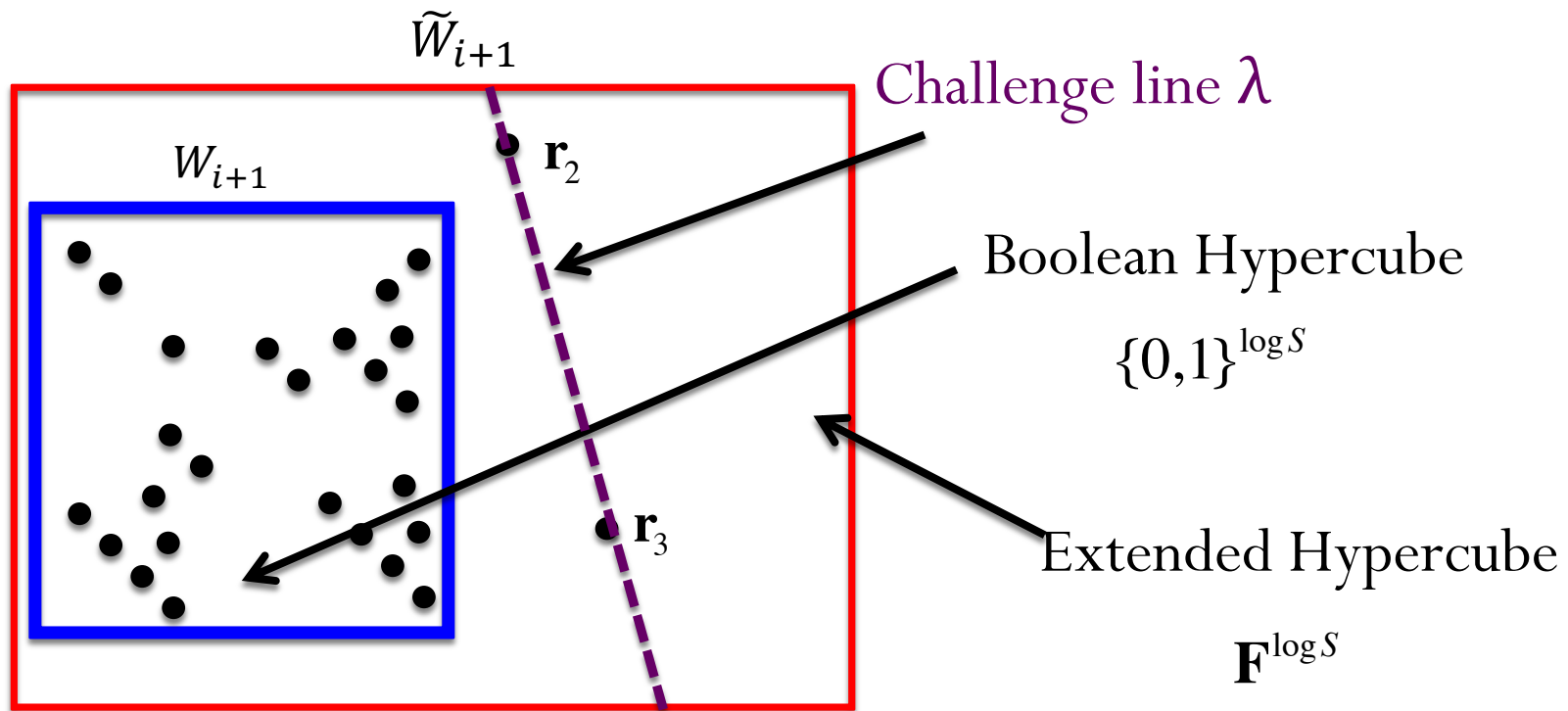
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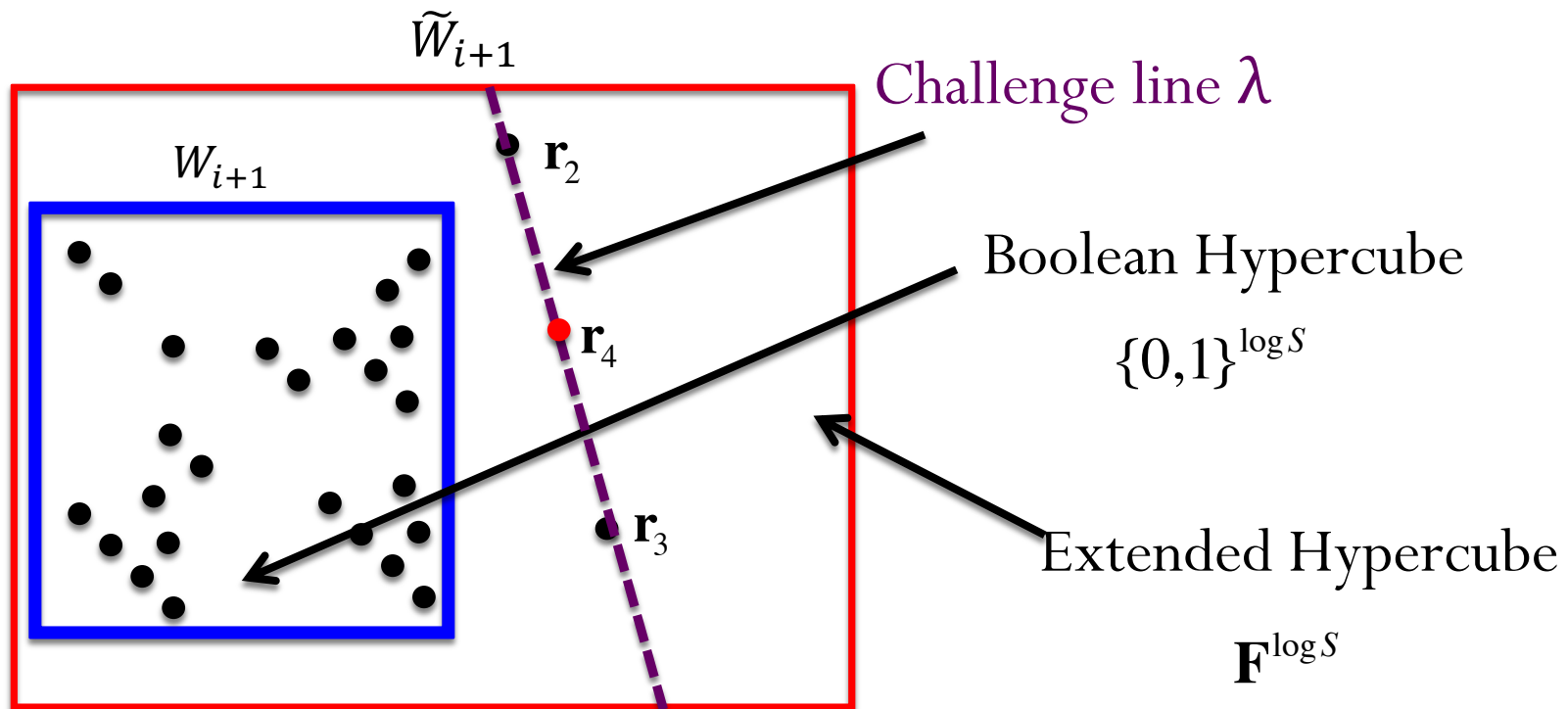
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Costs of the GKR protocol

- V time is $O(n + D \log S)$ where n is input size, D is circuit depth, and S is circuit size.
 - Assumes V can compute $\widetilde{\text{add}}_i(\mathbf{r}_1, \mathbf{r}_2, \mathbf{r}_3)$ and $\widetilde{\text{mult}}_i(\mathbf{r}_1, \mathbf{r}_2, \mathbf{r}_3)$ unaided in time $\text{polylog}(n)$
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- Communication cost is $O(D \log S)$.
- **P** time is $O(S)$.
 - A naïve implementation of **P** takes $\Omega(S^3)$ time, where S is circuit size.
 - A sequence of works has brought this down to $O(S)$, for arbitrary circuits [CMT12, Thaler13, WJBSTWW17, XZZPS19]



GKR Prover Runtime: Details

Recall: Core of the GKR protocol is applying sum-check to compute

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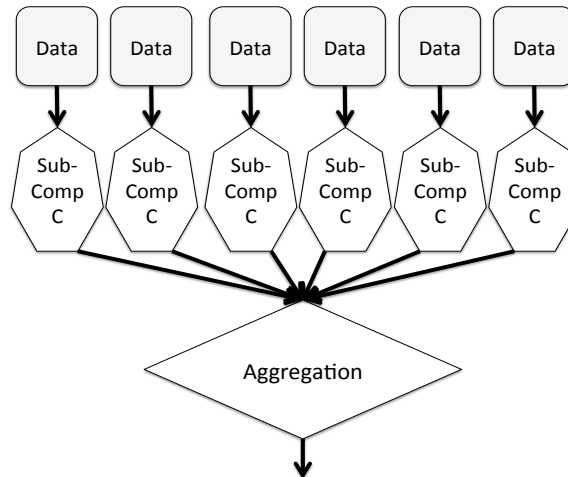
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- All subsequent works seek to bring “Approach 3” to bear on the GKR protocol, letting **P** reuse work across rounds.

GKR Prover Runtime: Details

- [Thaler13]:
 1. **P** time $O(S)$ for circuits with “nice” wiring patterns.
 2. **P** time $O(S \log S')$ for data parallel circuits
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Rumination on Generality Vs. Efficiency

Generality vs. Efficiency

- The GKR protocol for circuit evaluation has now been rendered optimally efficient for **P** (up to constant factors).
- **Any** computation can be represented as a circuit evaluation (or satisfiability) problem.
 - But this can introduce tremendous overheads.
 - The GKR protocol **forces** the prover to compute the output in a prescribed manner, which may be far from optimal (gate-by-gate evaluation of a circuit).
- To achieve scalability, the gold standard is really something like the counting triangles protocol.
 - i.e., **P** computed the right answer directly using the fastest known algorithm, and did a low-order amount of extra work to prove correctness

Succinct ZK Arguments for Circuit-SAT from Interactive Proofs

Succinctness for Circuit-SAT

- The GKR protocol solves arithmetic circuit **evaluation**.
- Applications often require solving circuit **satisfiability**, i.e., given a circuit C and public input x , prove there exists a “witness” w such that $C(x, w) = y$.
- Naïve approach: have **P** send w to **V** and then apply the GKR protocol to check that $C(x, w) = y$.
 - Downside: Proof length is $|w|$.
- [ZGKPP17]: Can decrease the proof length by having the prover **cryptographically commit** to w , without sending w in full to **V**.

Known Cryptographic Commitment Schemes for Multilinear Polynomials

1. [KZG 2010, PST 2013, ZGKPP17]: Simple, based on bilinear maps, requires trusted setup (SRS of size $|w|$), not quantum secure.
2. [Groth09, BG12, BCCGP16, BG18, BBBPWM18, WSTTW18]: Based on homomorphic commitments. Transparent but not quantum secure.
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- **P** complexity in 1) and 2): $O(|w|)$ **public-key** crypto operations.
 - **P** complexity in 3): $O(|w| \log |w|)$ field operations and $O(|w|)$ **private-key** crypto operations.
 - Not discussed: **V** time, restrictions on the field size, etc.

Succinctness for Circuit-SAT

- Assume for simplicity that $|x| = |w| = n$.
- When applying the GKR protocol to check that $C(x, w) = y$, V views the input $z := (x, w)$ as a function mapping $\{0,1\} \times \{0,1\}^{\log n}$ to F .
 - And the only information V needs to know about z is $\tilde{z}(\mathbf{r})$ for a random input $\mathbf{r} = (r_1, \mathbf{r}') \in \{0,1\} \times \{0,1\}^{\log n}$.
 - **Fact:** $\tilde{z}(\mathbf{r}) = (1 - r_1) \tilde{x}(\mathbf{r}') + r_1 \tilde{w}(\mathbf{r}')$.

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 - **Fact:** $\tilde{z}(\mathbf{r}) = (1 - r_1) \tilde{x}(\mathbf{r}') + r_1 \tilde{w}(\mathbf{r}')$.
- The Argument for CIRCUIT-SAT:
 1. P cryptographically commits to the multilinear polynomial $\tilde{w}$.
 2. V and P apply to GKR protocol to the claim $C(x, w) = y$.
 3. To perform V 's final check in the protocol (which requires knowing $\tilde{z}(\mathbf{r})$), V makes P reveal $\tilde{w}(\mathbf{r}')$, and derives $\tilde{z}(\mathbf{r})$ using **Fact**.

Zero Knowledge for Circuit-SAT

- Two practical techniques:
 1. [WsTTW18, ZGKPP18]: Efficient implementation of Cramer-Damgård transformation (based on homomorphic commitments).
 2. [CFS17, XZZPS19] IOP-based transformation.
- Both transparent. Only 2) is quantum secure.

THANK YOU!

MIPs and Succinct Arguments Derived Thereof

Arithmetic Circuit Satisfiability

- Given: An arithmetic circuit C over $\mathbf{F}$ of size S with explicit input x and non-deterministic input w , and claimed output(s) y .
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- Assign each gate in C a $(\log S)$ -bit label.
- Call a function $W : \{0,1\}^{\log S} \rightarrow \mathbf{F}$ a *transcript* for C .
- Say that W is *correct* on x if it satisfies the following properties:
 - The values W assigns to the explicit input gates equal x .
 - The value W assigns to the output gates is y .
 - The values W assigns to the intermediate gates correspond to the correct operation of the gates.
 - Clearly there is a w such that $C(x, w) = y$ iff there is a correct transcript for C .

Sketch of 2-Prover MIP for Arithmetic Circuit SAT

[Blumberg, Thaler, Vu, Walfish, 2014]

- Protocol Sketch:
 - P_1 and P_2 claim to hold an extension Z of a correct transcript W for C .
 - Identify a polynomial $g_{x,Z} : \{0,1\}^{3\log S} \rightarrow \mathbf{F}$ (that depends on x and Z) such that:
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 - To perform final check in sum-check protocol, V needs to evaluate $g_{x,Z}^2$ at a random point. But this requires evaluating Z at a random point, and Z only “exists” in P_1 ’s head.
 - So V asks P_2 for the evaluation of Z .
 - Soundness analysis of sum-check is valid as long as P_2 ’s claim about Z is consistent with a **low-degree** polynomial. So V also runs a **low-degree test** with P_1 and P_2 .

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- So we define:

$$g_{x,Z}(\mathbf{a}, \mathbf{b}, \mathbf{c}) = \tilde{\text{io}}(\mathbf{a}, \mathbf{b}, \mathbf{c}) \cdot (\tilde{I}_x(\mathbf{a}) - Z(\mathbf{a})) + \tilde{\text{add}}(\mathbf{a}, \mathbf{b}, \mathbf{c})(Z(\mathbf{a}) - (Z(\mathbf{b}) + Z(\mathbf{c}))) + \tilde{\text{mult}}(\mathbf{a}, \mathbf{b}, \mathbf{c}) \cdot (Z(\mathbf{a}) - Z(\mathbf{b}) \cdot Z(\mathbf{c}))$$

Costs of the 2-Prover MIP for Non-Deterministic Circuit Evaluation

Rounds	V Time	P_1 Time	P_2 Time
$\log S$	$O(n + \log^2 S)$	$O(S)$	$O(S \log S)$

[RRR16] and Open Questions

Another General-Purpose Doubly-
Efficient Interactive Proof

What We Really Want

- In the cloud computing scenario at the start of the talk, we really wanted the following:
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 2. P proves that she correctly ran the program on the data.
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 - So we can only hope to achieve a linear-time verifier for problems solvable in quadratic space.
 - [RRR16] come close to achieving the best we can hope for.

[RRR16]

- Let f be a problem solvable in time T and space S . Then for any constant $\varepsilon > 0$, f has an interactive proof where:
 - V runs in time $\tilde{O}(n + T^\varepsilon \cdot \text{poly}(s))$.
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- In particular, if $T = \text{poly}(n)$ and S is a small enough polynomial in n , then this is a doubly-efficient interactive proof system.
- The number of rounds is **constant**.
 - More precisely, it is $\exp\left(\frac{1}{\varepsilon}\right)$.

Open Questions (Theory)

- Improve V 's runtime in [RRR16] from $\tilde{O}(n + T^\varepsilon \cdot \text{poly}(s))$ to $\tilde{O}(n + \text{poly}(s, \log T))$? Maybe even $\tilde{O}(n + s \cdot \log T)$?
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- Improve the round complexity from $\exp\left(\frac{1}{\varepsilon}\right)$ to $\text{poly}\left(\frac{1}{\varepsilon}\right)$?
- Give an interactive proof for **batch-verification of NP statements**?
 - Under standard complexity assumptions, interactive proofs cannot be **succinct** [GH98, GVW01].
 - I.e., for a general **NP** relation, cannot do much better than just having the prover send the **NP** witness to the verifier.

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 - I.e., for a general **NP** relation, cannot do much better than just having the prover send the **NP** witness to the verifier.
 - Open: given k instances of the same **NP** problem, is there an interactive proof for verifying that the answer to all k instances is YES, with communication that grows sublinearly with k ?

A Parting Remark

- We've seen some fundamental limitations of interactive proofs.
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 - V can't run in linear time for space-intensive problems.
 - They cannot be succinct.
 - They are interactive.
 - They are not publicly verifiable.
- All of these limitations can be addressed by combining interactive proofs with cryptography.
 - This yields **succinct non-interactive arguments**.
 - See tomorrow's talks.
- There are many practically-relevant open questions about the best way to combine interactive proofs with cryptography.

THANK YOU!

A Simple Triangles Protocol with Sub-Optimal Prover Time

Counting Triangles

- Input: $A \in \{0,1\}^{n \times n}$, representing the adjacency matrix of a graph.
- Desired Output: $\frac{1}{6} \cdot \sum_{(i,j,k) \in [n]^3} A_{ij} A_{jk} A_{ik}$.
- Fastest known algorithm runs in matrix-multiplication time, currently about $n^{2.37}$.

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- The Protocol:
 - View A as a function mapping $\{0,1\}^{\log n} \times \{0,1\}^{\log n}$ to $\mathbf{F}$.
 - Recall that $\tilde{A}$ denotes the multilinear extension of A .
 - Define the polynomial $g(X, Y, Z) = \tilde{A}(X, Y) \tilde{A}(Y, Z) \tilde{A}(X, Z)$
 - Apply the sum-check protocol to g to compute:

$$\sum_{(a,b,c) \in \{0,1\}^{3 \log n}} g(a, b, c)$$

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- Costs:
 - Total communication is $O(\log n)$, $\mathbf{V}$ runtime is $O(n^2)$, $\mathbf{P}$ runtime is $O(n^3)$.
 - $\mathbf{V}$'s runtime dominated by evaluating:

$$g(r_1, r_2, r_3) = \tilde{A}(r_1, r_2) \tilde{A}(r_2, r_3) \tilde{A}(r_1, r_3).$$